

2815

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Attorney Docket No. 037267-0135

Takaaki Nagai and Masahiro Shinmori

Group Art Unit: 2815

Serial No. 09/606,159

Examiner: P. Brock II

Filed: June 29, 2000

For: EEPROM SEMICONDUCTOR DEVICE AND METHOD OF FABRICATING THE SAME

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TRANSMITTAL

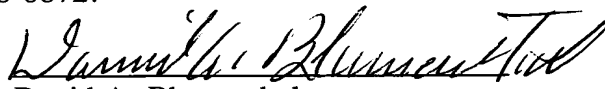
Commissioner for Patents
Washington, D.C. 20231

Transmitted herewith is an Amendment, for the above-captioned application. The fees have been calculated as shown below.

CLAIMS AS AMENDED						
(1)	(2) Claims Remaining After Amendment	(3)	(4) Highest Number Previously Paid For	(5) Extra Claims	(6) Rate	(7) Fee
Total Claims (Small Entity)	6	-	20	0	18.00 (9.00)	0
Independent claims (Small Entity)	2	-	3	3	80.00 (40.00)	0
Multiple Dependent (Small Entity)	0	-	0	0	270.00 (135.00)	0
Extension of Time Fee (Small Entity)	One Month \$110 (\$55)		Two Months \$390 (\$195)	Three Months \$890 (\$445)		0
Total						0

No fee is due at this time. However, the Commissioner is hereby authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-0872.

April 4 2001
Date


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THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No. 037267-0135

In re patent application of

Takaaki Nagai and Masahiro Shinmori

Group Art Unit: 2815

Serial No. 09/606,159

Examiner: P. Brock II

Filed: 6/29/00

For: EEPROM SEMICONDUCTOR DEVICE AND METHOD OF FABRICATING
THE SAME

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CERTIFICATE OF MAILING

Assistant Commissioner for Patents
Washington, D.C. 20231


Assistant Commissioner:

I hereby certify that the following paper(s) and/or fee along with any attachments referred to or identified as being attached or enclosed are being deposited with the United States Postal Service as First Class Mail under 37 C.F.R. § 1.8(a) on the date of deposit shown below with sufficient postage and in an envelope addressed to the Assistant Commissioner for Patents, Washington D.C. 20231.

1. Amendment and Request for Reconsideration under 37 C.F.R. § 1.111
2. Mark-up Copy of Specification
3. Postcard

Respectfully submitted,

April 4, 2000
Date


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#5/Amend B
Am
4/12/01

UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Takaaki Nagai and Masahiro Shinmori
Serial No.: 09/606,159
Filed: 6/29/00
Title: EEPROM SEMICONDUCTOR DEVICE AND METHOD OF FABRICATING THE SAME

Examiner: P. Brock
Group Art Unit: 2815
Docket No.: 037267-0135

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AMENDMENT

In response to the Office Action mailed January 4, 2001, please amend the above-identified application as follows:

IN THE SPECIFICATION:

On page 1, line 29, replace the paragraph as follows:

As illustrated in Fig. 3A, a p-type well 2 is formed in a p-type semiconductor substrate 1 in a region where a memory cell array is to be formed. Then, a plurality of field insulating films 3 is formed in the form of islands by selective oxidation. The field insulating films are not illustrated in Fig. 3A, but are insulated in Fig. 2.

On page 3, line 25, replace the paragraph as follows:

As a result, as illustrated in Fig. 4 which is a cross-sectional view taken along the line IV-IV in Fig. 1, there is formed an undesirable recess 19 at a surface of the semiconductor substrate 1. The undesirable recess 19 causes junction leakage therein, which poses a problem that data-writing and data-eliminating properties are deteriorated.

On page 4, line 20, replace the paragraph as follows:

As illustrated in Fig. 8A, a p-type well 2 is formed in a p-type semiconductor substrate 1 by introducing p-type impurities into the semiconductor substrate